Compact Chip Resistor Networks MNR04 (1005 × 4 size)

Features

1) Extremely small and light

- Area ratio is 60% smaller than that of chip 3216 (MNR14), while weight ratio has been cut 75%.
- 2) High-density mounting

Can be mounted even more densely than four 1005 chips (MCR01), and mounting costs are lower.

- Can be mounted on a wide variety of devices
 Squared corners make it excellent for mounting on image recognition devices.
- 4) Convex electrodes

Easy to check the fillet after soldering is finished.

5) ROHM resistors have approved ISO9001- / ISO/TS 16949- certification.

Furthermore, changes to the design and specifications of products may occur without notice. Therefore, before ordering or using this product, please make sure to reconfirm the specification sheet before ordering or using this product.

Item	Conditions	Specifications	
Rated power	Power must be derated according to the power derating curve in Figure 1 when ambient temperature exceeds 70°C.	0.063W (1 / 16W) at 70°C	
Rated voltage	The voltage rating is calculated by the following equation. If the value obtained exceeds the limiting element voltage, the voltage rating is equal to maximum operating voltage. $E = \sqrt{P \times R} \qquad \begin{array}{c} E : \text{Voltage rating (V)} \\ P : \text{Power rating (W)} \\ R : \text{Nominal resistance } (\Omega) \end{array}$	Limiting element voltage 25V	
Nominal resistance	See Table 1.		
Operating temperature		–55°C to +125°C	

Ratings

Resistors

Jumper type		Table 1			
Resistance	Max.50mΩ	Resistance tolerance	Resistance range		Resistance temperature
Rated current	1A		(Ω)		coefficient (ppm / °C)
Operating temperature	–55°C to +125°C	J (±5%)	10≤R≤1M	(E24)	±200
Operating temperature	-55 0 10 +125 0				

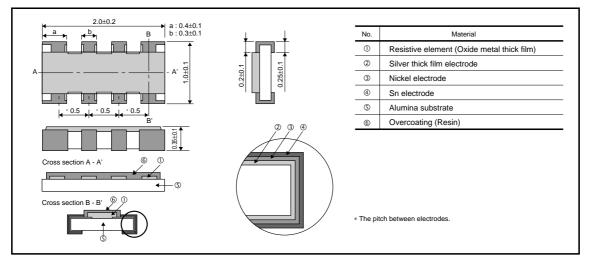
•Before using components in circuits where they will be exposed to transients such as pulse loads (short-duration, high-level loads), be certain to evaluate the component in the mounted state. In addition, the reliability and performance of this component cannot be guaranteed if it is used with a steady state voltage that is greater than its rated voltage.

Characteristics

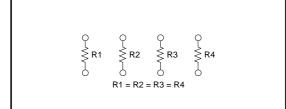
ltom	Guaranteed value		Test conditions (US C 5201.1)	
Item	Resistor type	Jumper type	Test conditions (JIS C 5201-1)	
Resistance	J : ±5%	Max. 50mΩ	JIS C 5201-1 4.5	
Variation of resistance with temperature	See Table.1		JIS C 5201-1 4.8 Measurement : -55 / +25 / +125°C	
Overload	± (2.0%+0.1Ω)	Max. 50mΩ	JIS C 5201-1 4.13 Rated voltage (current) ×2.5, 2s. Limiting Element Voltage×2 : 50V	
Solderability	A new uniform coating of minimum of 95% of the surface being immersed and no soldering damage.		JIS C 5201-1 4.17 Rosin-Ethanol (25%WT) Soldering condition : 235±5°C Duration of immersion : 2.0±0.5s.	
Resistance to soldering heat	$\begin{array}{c c} \pm (1.0\% + 0.05 \Omega) & Max. \ 50m\Omega \\ & \text{No remarkable abnormality on the appearance.} \end{array}$		JIS C 5201-1 4.18 Soldering condition : 260±5°C Duration of immersion : 10±1s.	
Rapid change of temperature	± (1.0%+0.05Ω)	Max. 50mΩ	JIS C 5201-1 4.19 Test temp. : -55°C to +125°C 5cyc	
Damp heat, steady state	± (3.0%+0.1Ω)	Max. 50mΩ	JIS C 5201-1 4.24 40°C, 93%RH Test time : 1,000h to 1,048h	
Endurance at 70°C	± (3.0%+0.1Ω)	Max. 50mΩ	JIS C 5201-1 4.25.1 Rated voltage (current), 70°C 1.5h : ON – 0.5h : OFF Test time : 1,000h to 1,048h	
Endurance	± (3.0%+0.1Ω)	Max. 50mΩ	JIS C 5201-1 4.25.3 125°C Test time : 1,000h to 1,048h	
Resistance to solvent	± (1.0%+0.05Ω)	Max. 50mΩ	JIS C 5201-1 4.29 23±5°C, Immersion cleaning, 5±0.5min. Solvent : 2-propanol	
Bend strength of the end face plating	± (1.0%+0.05Ω) Without mechanical	Max. 50mΩ damage such as breaks.	JIS C 5201-1 4.33	

Resistors

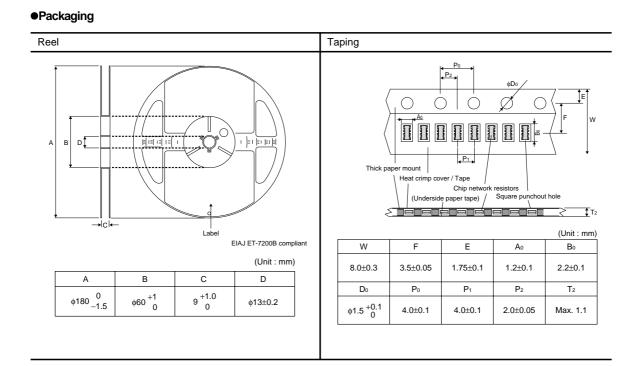
•Dimensions (Unit : mm)



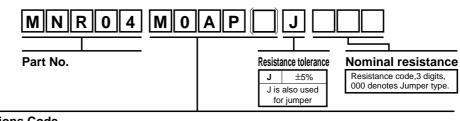
Equivalent circuit



Resistors



Part No. Explanation



Packaging Specifications Code

Part No.	Code	Resistance tolerance J (±5%)	Packaging specifications	Reel	Basic ordering unit (pcs)
MNR04	M0AP	O	Paper tape (2mm Pitch)	φ180mm (7inch)	10,000

Reel (\#180mm) : Compatible with JEITA standard "EIAJ ET-7200B" ③ : Standard product

Notes

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Appendix1-Rev2.0

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